

INTERNATIONAL STANDARD

IEC 60068-2-77

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Environmental testing – Part 2-77: Tests – Test 77 – Body strength and impact shock

Essais d'environnement –

Partie 2-77:

*Essais – Essai 77 – Résistance du corps
et résistance au choc par impact*

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

ENVIRONMENTAL TESTING –

Part 2-77: Tests – Test 77: Body strength and impact shock

FOREWORD

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International Standard IEC 60068-2-77 has been prepared by IEC technical committee 50: Environmental testing, and is published by IEC technical committee 91: Surface mounting technology.

The text of this standard is based on the following documents:

FDIS	Report on voting
91/155/FDIS	91/162/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

Annex A is for information only.

ENVIRONMENTAL TESTING –

Part 2-77: Tests – Test 77: Body strength and impact shock

1 Scope and object

This part of IEC 60068 provides test methods applicable to surface mounting devices (SMDs) made of glass or sintered materials such as capacitors, resistors and inductors incorporating ferrites. Two test methods exist: body strength and impact shock.

The object of both tests is to evaluate the mechanical stresses applied to SMDs during and after mounting; these tests look at different mechanical stresses. The relevant component specification shall specify which test method or methods are applicable.

2 Normative reference

The following normative document contains provisions which, through reference in this text, constitute provisions of this part of IEC 60068. At the time of publication, the edition indicated was valid. All normative documents are subject to revision, and parties to agreements based on this part of IEC 60068 are encouraged to investigate the possibility of applying the most recent edition of the normative document indicated below. Members of IEC and ISO maintain registers of currently valid International Standards.

IEC 60194:—, *Terms and definitions for printed circuits*