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IEC 61188-5-4

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# INTERNATIONAL STANDARD

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**Printed boards and printed board assemblies – Design and use –  
Part 5-4: Attachment (land/joint) considerations – Components with J leads on  
two sides**

INTERNATIONAL  
ELECTROTECHNICAL  
COMMISSION

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DESIGN AND USE –**

**Part 5-4: Attachment (land/joint) considerations –  
Components with J leads on two sides**

FOREWORD

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International Standard IEC 61188-5-4 has been prepared by IEC technical committee 91: Electronics assembly technology

The text of this standard is based on the following documents:

FDIS	Report on voting
91/703/FDIS	91/735/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts of the IEC 61188 series, under the general title *Printed boards and printed board assemblies – Design and use*, can be found on the IEC website.

The committee has decided that the contents of this publication will remain unchanged until the maintenance result date indicated on the IEC web site under "<http://webstore.iec.ch>" in the data related to the specific publication. At this date, the publication will be

- reconfirmed;
- withdrawn;
- replaced by a revised edition, or
- amended.

A bilingual version of this standard may be issued at a later date.

## INTRODUCTION

This part of IEC 61188 covers land pattern for components with J leads on two sides.

The proposed land pattern dimensions in this standard are based upon the fundamental tolerance calculation combined with the given land protrusions and courtyard excesses (see IEC 61188-5-1, Generic requirements). The courtyard includes all issues of the normal manufacturing necessities.

The unaltered land pattern dimensions of this part are generally applicable for the solder paste application plus reflow soldering process. For application of the wave soldering process (though uncommon for SOJ components) the land pattern and courtyard dimensions may have to be modified. An orientation parallel to the wave direction is strongly recommended and suitably dimensioned solder thieves should be added.

This standard offers a threefold land pattern dimensioning (levels 1, 2, 3) on the basis of a threefold set of land protrusions and courtyard excesses: maximum (max.); medium (mdn); and minimum (min.). Nevertheless the user may develop deviating land pattern dimensions based upon the methodology of IEC 61188-5-1, introducing his own special material and assembling process conditions C, F, P and perhaps his own special land protrusions and courtyard excesses dimensions, as required.

If a user has good reasons to use a concept different from that of IEC 61188-5-1 or if the user prefers unusual land protrusions, this standard should be used for checking the resulting solder fillets.

It is the responsibility of the user to verify his used SMD land patterns for achieving an undisturbed mounting process including testing and an ensured reliability for the product stress conditions in use.

## **PRINTED BOARDS AND PRINTED BOARD ASSEMBLIES – DESIGN AND USE –**

### **Part 5-4: Attachment (land/joint) considerations – Components with J leads on two sides**

#### **1 Scope**

This part of IEC 61188 provides the component and land pattern dimensions for small outline integrated circuits with “J” leads on two sides (SOJ components) used in the reflow soldering process. Basic construction of the SOJ device is also covered. Clause 4 lists the tolerances and target solder joint dimensions used to arrive at the land pattern dimensions.

#### **2 Normative references**

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60068-2-58, *Environmental testing – Part 2-58: Tests: Test Td. Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)*

IEC 60286-3, *Packaging of components for automatic handling – Part 3: Packaging of surface mount components on continuous tapes*

IEC 60286-4, *Packaging of components for automatic handling – Part 4: Stick magazines for electronic components encapsulated in packages of form E and G*

IEC 60286-5, *Packaging of components for automatic handling – Part 5: Matrix trays*

IEC 61760-1, *Surface mounting technology – Part 1: Standard method for the specification of surface mounting components (SMDs)*