



# TECHNICAL REPORT



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**Process management for avionics – Electronics design –  
Part 1: Electrical signal properties, naming conventions and interface control  
document (ICD)**

INTERNATIONAL  
ELECTROTECHNICAL  
COMMISSION

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ELECTRONICS DESIGN –**

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FOREWORD

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IEC TR 63238-1, which is a Technical Report, has been prepared by IEC technical committee 107: Process management for avionics.

The text of this Technical Report is based on the following documents:

Draft TR	Report on voting
107/351/DTR	107/356/RVDTR

Full information on the voting for the approval of this Technical Report can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 63238 series, published under the general title *Process management for avionics – Electronics design*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "<http://webstore.iec.ch>" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

A bilingual version of this publication may be issued at a later date.

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## **PROCESS MANAGEMENT FOR AVIONICS – ELECTRONICS DESIGN –**

### **Part 1: Electrical signal properties, naming conventions and interface control document (ICD)**

#### **1 Scope**

This part of IEC 63238 provides information and a template to create an interface control document (ICD) for any project which includes electronic assemblies, such as electronic circuit card assemblies (CCAs) or electronic devices, connected together. This document proposes electrical signal naming conventions when interfacing electronic assemblies, and an example containing seven signal naming conventions is included. This document supports original equipment manufacturers (OEMs) in the preparation and maintenance of their electronic assemblies interfaces and integration specifications to avoid misunderstanding of signals which can cause unnecessary design and/or integration errors, and testing complications.

#### **2 Normative references**

There are no normative references in this document.