

INTERNATIONAL  
STANDARD

**ISO/IEC**  
**11573**

First edition  
1994-12-15

---

---

**Information technology —  
Telecommunications and information  
exchange between systems —  
Synchronization methods and technical  
requirements for Private Integrated  
Services Networks**

*Technologies de l'information — Télécommunications et échange  
d'information entre systèmes — Méthodes de synchronisation et  
exigences techniques pour les réseaux privés avec intégration de services*



Reference number  
ISO/IEC 11573:1994(E)

## Contents

	Page
<b>Section 1 : General</b>	
1.1 Scope .....	1
1.2 Definitions .....	1
1.3 Abbreviations and acronyms .....	3
1.4 Impact of slips .....	4
<b>Section 2 : Technical requirements, Synchronization methods</b>	
2.1 Technical requirements .....	5
2.1.1 Jitter and wander at the input .....	5
2.1.1.1 C0 and T0 interfaces (144 kbits/s) .....	5
2.1.1.2 C1 and T1 interfaces (1,544 Mbits/s) .....	5
2.1.1.3 C2 and T2 interfaces (2,048 Mbits/s) .....	6
2.1.2 Jitter and wander at the output .....	6
2.1.2.1 C0 and T0 interfaces (144 kbits/s) .....	6
2.1.2.2 C1 and T1 interfaces (1,544 Mbits/s) .....	6
2.1.2.3 C2 and T2 interfaces (2,048 Mbits/s) .....	7
2.1.3 Frequency deviation at the input .....	7
2.1.4 Accuracy .....	7
2.1.5 Lock range .....	7
2.1.6 Phase discontinuity of slave clocks .....	7
2.2 Synchronization methods for PISNs .....	8
2.2.1 High level concepts .....	8
2.2.2 Reference Clock Switching Criteria .....	8
2.2.3 Reference Restoral .....	9
2.2.4 Timing Reference Interfaces and Alarms .....	9
2.2.5 Buffers .....	9
2.2.6 Controls .....	9
2.2.7 Slip performance objectives .....	9
2.2.8 Strategies .....	10
<b>Section 3 : Description of the synchronization methods</b>	
3.1 Plesiochronous operation .....	11
3.2 Synchronization from one input .....	11
3.3 Automatic switch over with signalling .....	12
3.4 Automatic switch over without signalling .....	12
<b>Annexes</b>	
<b>A Choice of clock references</b>	
A.1 Choice of reference from public nodes .....	13
A.2 Choice of references between private nodes .....	14
A.3 Avoidance of Timing Loops .....	15
<b>B Synchronization configuration</b>	
B.1 Master Slave configurations (synchronization) .....	16
B.2 master–master configuration (split timing) .....	17

<b>C Basis of strategies</b>	
C.1 Slip rate	18
C.2 Allocation of the controlled slips	18
C.3 Unavailability of the links	19
C.4 Nodal solutions	20
C.5 Description of the five options	21
<b>D Synchronized Private Network Examples</b>	
D.1 Example with a small private network	22
D.2 Example with a big private network	22
D.3 Example with two different public clock sources	23
D.4 Example with a transit node	23
<b>E Slave Clock Performance Measurement Guidelines</b>	
E.1 Slave Clocks considerations	24
E.1.1 Ideal Operation	24
E.1.2 Stressed Operation	25
E.1.3 Holdover Operation	25
E.2 Test Configuration Guidelines	25
E.2.1 Reference Clock	25
E.2.2 Digital Reference Simulation	26
E.2.3 Output Timing Signal Recovery	26
E.3 Test categories	26
E.3.1 Ideal Testing	26
E.3.2 Stress Testing	26
E.3.3 Holdover Testing	27
<b>F Signalling for management of synchronization</b>	
F.1 Presentation	28
F.1.1 Configuration parameters	28
F.1.2 Reactions of the node	28
F.1.3 Reference clock switching and restoral	28
F.2 Description of the states	28
F.2.1 Initial states	28
F.2.2 Slave states	29
F.2.3 Autonomous state	29
F.2.4 Wait states	29
F.3 Description of the events	29
F.3.1 Failure of links	29
F.3.2 Signalling information	29
F.3.3 Time out	29
F.4 SDL representation of the state machine	29
<b>G Bibliography</b>	34

## Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for worldwide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work.

In the field of information technology, ISO and IEC have established a joint technical committee, ISO/IEC JTC 1. Draft International Standards adopted by the joint technical committee are circulated to national bodies for voting. Publication as an International Standard requires approval by at least 75 % of the national bodies casting a vote.

International Standard ISO/IEC 11573 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 6, *Telecommunications and information exchange between systems*.

During the preparation of this International Standard, information was gathered on patents upon which application of the standard might depend. Relevant patents were identified as belonging to ALCATEL Business Systems. However, ISO and IEC cannot give authoritative or comprehensive information about evidence, validity or scope of patent and like rights. The patent-holder has stated that licences will be granted under reasonable terms and conditions and communications on this subject should be addressed to

ALCATEL Business Systems  
Business Products Division  
54, avenue Jean Jaurès  
92707 Colombes Cedex  
France  
Tel. : (1) 47.85.55.55  
Telex: 615.531 F  
Telefax: (1) 47.85.54.20

## Introduction

When synchronous digital signals are being transported over a communications link, the receiving end must operate at the same average frequency as the transmitting end to prevent loss of information. This is referred to as link synchronization. When digital signals traverse a network of digital communications links, switching nodes, multiplexers, and transmission interfaces, the task of keeping all the entities operating at the same average frequency is referred to as network synchronization.

The design of a PISN requires specification of the timing sources and receivers for the synchronization network. Proper design requires that timing loops in the synchronization network be avoided. A timing loop occurs when a clock is using as its reference frequency a signal that is itself traceable to the output of that clock. The formation of such a closed timing loop leads to frequency instability and is not permitted. While it is relatively straightforward to ensure against timing loops in the primary synchronization reference network, care should be taken that timing loops do not occur during failure or error conditions when various timing references are rearranged.

When a PISN is not connected to the public digital network, synchronization can be achieved by having all PISN equipment derive timing from a single source. This source should be the highest quality clock available. Alternatively, if timing is derived from more than one class I clock, or public clock traceable source, the network is said to be operating *plesiochronously*.

If a PISN is connected to the public network at one or more nodes, the private network designer can coordinate with the public network provider to derive class I clock, or public clock traceable timing from the public digital network. More information is available in Annex A.

# Information technology — Telecommunications and information exchange between systems — Synchronization methods and technical requirements for Private Integrated Services Networks

## Section 1 : General

### 1.1 Scope

This International Standard contains requirements necessary for the synchronization of PISNs. Timing within a digital private network needs to be controlled carefully to ensure that the rate of occurrence of slips between PINXs within the PISN, and the public switched networks is sufficiently low not to affect unduly the performance of voice transmissions, or the accuracy or throughput (if errored data require re-transmission) of non-voice services.

Requirements are also based upon the interconnection of digital private telecommunication networks via digital facilities in the public (switched or not) telecommunication networks.

This International Standard is one of a series of technical standards on telecommunications networks. This International Standard with its companion standards fills a recognized need in the telecommunications industry brought about by the increasing use of digital equipment and facilities in private networks. It is useful to anyone engaged in the manufacture of digital customer premises equipment (CPE) for private network applications, and to those purchasing, operating or applying digital CPE to digital facilities for Private Integrated Services Networks (PISN).

This International Standard establishes technical criteria necessary in the design of a synchronization plan for a PISN. Compliance with these requirements would be expected to result in a quality PISN synchronization design.